

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	74542	oscillator near5 (control or controlling or controlable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:48
L2	227679	frequency near5 (control or controlling or controlable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:48
L3	1883921	(states or signals) with (control or controlling or controlable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:51
L4	121021	switch with capacitor	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:49
L5	19549	1 same 2 same 3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:50
L6	2060	4 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:50
L8	1669627	(states or signals) near10 (control or controlling or controlable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 13:00
L10	2017	6 and 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:53

L11	19266	current adj frequency	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:54
L12	59233	frequency near5 compar\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:55
L13	4610	frequency with (state near10 select\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:56
L14	211	1 and 12 and 13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:58
L15	33776	1 with 8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:59
L16	1679	10 and 15	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 12:59
L17	1379020	(states or signals) near5 (control or controlling or controlable)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 13:01
L18	1637	16 and 17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 13:03

L19	317902	frequency near5 (particular or distict or predefine\$1 or differen\$2 or predetermin\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 13:06
L21	10697	1 same 19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 13:07
L22	38887	2 same 19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 13:08
L23	579	18 and 21	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 13:08
L24	712	18 and 22	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/03/18 13:08
L27	144	@ad<"20010330" and 14	USPAT	OR	OFF	2005/03/18 13:09
L28	339	@ad<"20010330" and 23	USPAT	OR	OFF	2005/03/18 13:10
L29	415	@ad<"20010330" and 24	USPAT	OR	OFF	2005/03/18 13:10

Day : Friday
Date: 3/18/2005

Time: 11:06:40



PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = DAMGAARD

First Name = MORTEN

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>08640813</u>	<u>6019672</u>	150	05/08/1996	GRINDING/POLISHING COVER SHEET FOR PLACING ON A ROTATABLE GRINDING/ POLISHING DISC	DAMGAARD, MORTEN
<u>09044281</u>	<u>6005443</u>	150	03/19/1998	PHASE LOCKED LOOP FREQUENCY SYNTHESIZER FOR MULTI-BAND APPLICATION	DAMGAARD, MORTEN
<u>09057124</u>	<u>6208875</u>	150	04/08/1998	RF ARCHITECTURE FOR CELLULAR DUAL-BAND TELEPHONES	DAMGAARD, MORTEN
<u>09163798</u>	<u>6150890</u>	150	09/30/1998	DUAL BAND TRANSMITTER FOR A CELLULAR PHONE COMPRISING A PLL	DAMGAARD, MORTEN
<u>09203645</u>	<u>6415001</u>	150	12/01/1998	SYSTEM AND PROCESS FOR SHARED FREQUENCY SOURCE MULTI-BAND TRANSMITTERS AND RECEIVERS	DAMGAARD, MORTEN
<u>09260919</u>	<u>6360087</u>	150	03/02/1999	DIRECT CONVERSION RECEIVER	DAMGAARD, MORTEN
<u>09386257</u>	<u>6766178</u>	150	08/31/1999	RF ARCHITECTURE FOR CELLULAR MULTI-BAND TELEPHONES	DAMGAARD, MORTEN
<u>09386865</u>	<u>6658237</u>	150	08/31/1999	MULTI-BAND TRANSCEIVER UTILIZING DIRECT CONVERSION RECEIVER	DAMGAARD, MORTEN
<u>09387038</u>	<u>6516184</u>	150	08/31/1999	MULTI-BAND TRANSCEIVER HAVING MULTI-SLOT CAPABILITY	DAMGAARD, MORTEN
<u>09398911</u>	<u>6526265</u>	150	09/14/1999	WIRELESS TRANSMITTER HAVING A MODIFIED TRANSLATION LOOP	DAMGAARD, MORTEN

				ARCHITECTURE	
<u>09495993</u>	<u>6259752</u>	150	02/01/2000	SYSTEM FOR CANCELLING INTERNAL INTERFERENCE IN A RECEIVER	DAMGAARD, MORTEN
<u>09515538</u>	<u>6731693</u>	150	02/29/2000	SYSTEM OF AND METHOD FOR COMPENSATING A BASEBAND SIGNAL TO REDUCE THIRD ORDER MODULATION DISTORTION	DAMGAARD, MORTEN
<u>09515633</u>	<u>6720839</u>	150	02/29/2000	SYSTEM OF AND METHOD FOR REDUCING OR ELIMINATING THE UNWANTED SIDEBAND IN A SIGNAL DERIVED FROM THE OUTPUT OF A QUADRATURE MODULATOR	DAMGAARD, MORTEN
<u>09634081</u>	<u>6697613</u>	150	08/08/2000	SYSTEM FOR CANCELING INTERNAL INTERFERENCE IN A RECEIVER	DAMGAARD, MORTEN
<u>09650831</u>	<u>6650875</u>	150	08/30/2000	TRANSMITTER ARCHITECTURE HAVING A SECONDARY PHASE-ERROR CORRECTION LOOP INCLUDING AN AMPLITUDE RECONSTRUCTION SYSTEM	DAMGAARD, MORTEN
<u>09650987</u>	<u>6670849</u>	150	08/30/2000	SYSTEM FOR CLOSED LOOP POWER CONTROL USING A LINEAR OR A NON-LINEAR POWER AMPLIFIER	DAMGAARD, MORTEN
<u>09665820</u>	<u>6795712</u>	150	09/20/2000	SYSTEM FOR ALLOWING A TDMA/CDMA PORTABLE TRANSCEIVER TO OPERATE WITH CLOSED LOOP POWER CONTROL	DAMGAARD, MORTEN
<u>09666577</u>	<u>6633751</u>	150	09/21/2000	SYSTEM FOR A DUAL FEEDBACK TRANSLATION LOOP FOR POWER AMPLIFIER FEEDBACK CONTROL	DAMGAARD, MORTEN
<u>09666698</u>	<u>6792282</u>	150	09/21/2000	MULTIPLE STEP SWITCHED TRANSLATION LOOP FOR POWER AMPLIFIER FEEDBACK CONTROL	DAMGAARD, MORTEN
<u>09704930</u>	<u>6801784</u>	150	11/02/2000	CONTINUOUS CLOSED-LOOP POWER CONTROL SYSTEM INCLUDING MODULATION INJECTION IN A WIRELESS	DAMGAARD, MORTEN

				TRANSCEIVER POWER AMPLIFIER	
<u>09721576</u>	Not Issued	160	11/21/2000	DUAL BAND TRANSMITTER FOR A CELLULAR PHONE COMPRISING A PLL	DAMGAARD, MORTEN
<u>09823316</u>	Not Issued	030	03/30/2001	SYSTEM FOR CONTROLLING THE FREQUENCY OF AN OSCILLATOR	DAMGAARD, MORTEN
<u>09823680</u>	<u>6671500</u>	150	03/30/2001	FREQUENCY PLAN	DAMGAARD, MORTEN
<u>10207320</u>	Not Issued	030	07/29/2002	MIRROR TRANSLATION LOOP TRANSMITTER ARCHITECTURE	DAMGAARD, MORTEN
<u>10313611</u>	Not Issued	030	12/06/2002	POWER AMPLIFIER CONTROL DRIVER HAVING OVER-CURRENT PROTECTION AND LINEAR CONTROL	DAMGAARD, MORTEN
<u>10357125</u>	Not Issued	061	02/03/2003	SOFTWARE DEFINED MULTIPLE TRANSMIT ARCHITECTURE	DAMGAARD, MORTEN
<u>10370585</u>	Not Issued	041	02/24/2003	WIRELESS TRANSMITTER HAVING A MODIFIED TRANSLATION LOOP ARCHITECTURE	DAMGAARD, MORTEN
<u>10392102</u>	Not Issued	030	03/18/2003	CONTINUOUS CLOSED-LOOP POWER CONTROL SYSTEM INCLUDING MODULATION INJECTION IN A WIRELESS TRANSCEIVER POWER AMPLIFIER	DAMGAARD, MORTEN
<u>10885162</u>	Not Issued	020	07/06/2004	CONTINUOUS CLOSED-LOOP POWER CONTROL SYSTEM INCLUDING MODULATION INJECTION IN A WIRELESS TRANSCEIVER POWER AMPLIFIER	DAMGAARD, MORTEN
<u>10915186</u>	Not Issued	020	08/10/2004	SYSTEM AND METHOD FOR ALLOWING A TDMA/CDMA PORTABLE TRANSCEIVER TO OPERATE WITH CLOSED LOOP POWER CONTROL	DAMGAARD, MORTEN
<u>10963049</u>	Not Issued	020	10/12/2004	LOW IF RECEIVER SYSTEMS AND METHODS	DAMGAARD, MORTEN
<u>10297835</u>	Not Issued	041	12/11/2002	MULTI-ZONE GRINDING AND/OR POLISHING SHEET	DAMGAARD, MORTEN J

<u>09485730</u>	<u>6435958</u>	150	03/07/2000	ABRASIVE MEANS AND A GRINDING PROCESS	DAMGAARD, MORTEN J.
<u>09786632</u>	<u>6607429</u>	150	04/03/2001	SUPPORT FOR TEMPORARY FIXATION OF A SELF-STICKING ABRASIVE AND/OR POLISHING SHEET	DAMGAARD, MORTEN J.
<u>09148896</u>	Not Issued	161	09/08/1998	SUPPORT FOR TEMPORARY FIXATION OF A SELF-STICKING ABRASIVE AND/OR POLISHING SHEET TO A MOVABLE ABUTMENT IN AN ABRASION AND/OR POLISHING APPARATUS	DAMGAARD, MORTEN J.
<u>09341460</u>	<u>6116998</u>	150	07/13/1999	ATTACHMENT MEANS AND USE OF SUCH MEANS FOR ATTACHING A SHEET-FORMED ABRASIVE OR POLISHING MEANS TO A MAGNETIZED SUPPORT	DAMGAARD, MORTEN J.

Inventor Search Completed: No Records to Display.

Search Another: Inventor	Last Name	First Name	Search
	<input type="text" value="DAMGAARD"/>	<input type="text" value="MORTEN"/>	

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Friday
Date: 3/18/2005

Time: 11:07:03



PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = DOMINO

First Name = WILLIAM

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09495993	6259752	150	02/01/2000	SYSTEM FOR CANCELLING INTERNAL INTERFERENCE IN A RECEIVER	DOMINO, WILLIAM J
09621407	Not Issued	041	07/21/2000	SYSTEM AND APPARATUS FOR A DIRECT CONVERSION RECEIVER AND TRANSMITTER	DOMINO, WILLIAM J.
09634081	6697613	150	08/08/2000	SYSTEM FOR CANCELING INTERNAL INTERFERENCE IN A RECEIVER	DOMINO, WILLIAM J.
09650831	6650875	150	08/30/2000	TRANSMITTER ARCHITECTURE HAVING A SECONDARY PHASE-ERROR CORRECTION LOOP INCLUDING AN AMPLITUDE RECONSTRUCTION SYSTEM	DOMINO, WILLIAM J.
09650987	6670849	150	08/30/2000	SYSTEM FOR CLOSED LOOP POWER CONTROL USING A LINEAR OR A NON-LINEAR POWER AMPLIFIER	DOMINO, WILLIAM J.
09665820	6795712	150	09/20/2000	SYSTEM FOR ALLOWING A TDMA/CDMA PORTABLE TRANSCEIVER TO OPERATE WITH CLOSED LOOP POWER CONTROL	DOMINO, WILLIAM J.
09666577	6633751	150	09/21/2000	SYSTEM FOR A DUAL FEEDBACK TRANSLATION LOOP FOR POWER AMPLIFIER FEEDBACK CONTROL	DOMINO, WILLIAM J.
09666698	6792282	150	09/21/2000	MULTIPLE STEP SWITCHED TRANSLATION LOOP FOR POWER AMPLIFIER FEEDBACK CONTROL	DOMINO, WILLIAM J.
09672235	6405054	150	09/27/2000	APPARATUS FOR AND	DOMINO,

				METHOD OF IMPROVING EFFICIENCY OF TRANSCEIVERS IN RADIO PRODUCTS	WILLIAM J.
<u>09675239</u>	<u>6545554</u>	150	09/29/2000	DIFFERENTIAL OSCILLATOR	DOMINO, WILLIAM J.
<u>09704930</u>	<u>6801784</u>	150	11/02/2000	CONTINUOUS CLOSED-LOOP POWER CONTROL SYSTEM INCLUDING MODULATION INJECTION IN A WIRELESS TRANSCEIVER POWER AMPLIFIER	DOMINO, WILLIAM J.
<u>09718615</u>	<u>6466069</u>	150	11/21/2000	FAST SETTLING CHARGE PUMP	DOMINO, WILLIAM J.
<u>09823316</u>	Not Issued	030	03/30/2001	SYSTEM FOR CONTROLLING THE FREQUENCY OF AN OSCILLATOR	DOMINO, WILLIAM J.
<u>09823680</u>	<u>6671500</u>	150	03/30/2001	FREQUENCY PLAN	DOMINO, WILLIAM J.
<u>10207320</u>	Not Issued	030	07/29/2002	MIRROR TRANSLATION LOOP TRANSMITTER ARCHITECTURE	DOMINO, WILLIAM J.
<u>10233231</u>	Not Issued	094	08/30/2002	WIRELESS TRANSMITTER INCORPORATING A SYNCHRONOUS OSCILLATOR IN A TRANSLATION LOOP	DOMINO, WILLIAM J.
<u>10281486</u>	Not Issued	030	10/28/2002	FAST CLOSED-LOOP POWER CONTROL FOR NON-CONSTANT ENVELOPE MODULATION	DOMINO, WILLIAM J.
<u>10313611</u>	Not Issued	030	12/06/2002	POWER AMPLIFIER CONTROL DRIVER HAVING OVER-CURRENT PROTECTION AND LINEAR CONTROL	DOMINO, WILLIAM J.
<u>10357125</u>	Not Issued	061	02/03/2003	SOFTWARE DEFINED MULTIPLE TRANSMIT ARCHITECTURE	DOMINO, WILLIAM J.
<u>10392102</u>	Not Issued	030	03/18/2003	CONTINUOUS CLOSED-LOOP POWER CONTROL SYSTEM INCLUDING MODULATION INJECTION IN A WIRELESS TRANSCEIVER POWER AMPLIFIER	DOMINO, WILLIAM J.
<u>10885162</u>	Not Issued	020	07/06/2004	CONTINUOUS CLOSED-LOOP POWER CONTROL SYSTEM INCLUDING MODULATION	DOMINO, WILLIAM J.

				INJECTION IN A WIRELESS TRANSCIVER POWER AMPLIFIER	
<u>10915186</u>	Not Issued	020	08/10/2004	SYSTEM AND METHOD FOR ALLOWING A TDMA/CDMA PORTABLE TRANSCIVER TO OPERATE WITH CLOSED LOOP POWER CONTROL	DOMINO, WILLIAM J.
<u>10917552</u>	Not Issued	020	08/12/2004	SYSTEM FOR ADAPTIVELY FILTERING A RECEIVED SIGNAL IN A WIRELESS RECEIVER	DOMINO, WILLIAM J.
<u>10963049</u>	Not Issued	020	10/12/2004	LOW IF RECEIVER SYSTEMS AND METHODS	DOMINO, WILLIAM J.
<u>10972626</u>	Not Issued	020	10/25/2004	TRANSMIT-RECEIVE SWITCH ARCHITECTURE PROVIDING PRE-TRANSMIT ISOLATION	DOMINO, WILLIAM J.
<u>06553614</u>	<u>4553060</u>	150	11/21/1983	ELECTROMECHANICAL RESONATOR APPARATUS	DOMINO, WILLIAM J.
<u>08892444</u>	<u>6049724</u>	150	07/14/1997	ULTRA-CLEAN VOLTAGE POWER SUPPLY GENERATOR	DOMINO, WILLIAM J.
<u>08972311</u>	<u>6163706</u>	150	11/18/1997	APPARATUS FOR AND METHOD OF IMPROVING EFFICIENCY OF TRANSCIVERS IN RADIO PRODUCTS	DOMINO, WILLIAM J.
<u>09153008</u>	<u>6192228</u>	150	09/11/1998	METHOD AND APPARATUS FOR IMPROVING THE SENSITIVITY OF AN RF RECEIVER AT CERTAIN FREQUENCIES	DOMINO, WILLIAM J.
<u>09161014</u>	<u>6466772</u>	150	09/25/1998	APPARATUS AND METHOD FOR IMPROVING POWER CONTROL LOOP LINEARITY	DOMINO, WILLIAM J.
<u>09260919</u>	<u>6360087</u>	150	03/02/1999	DIRECT CONVERSION RECEIVER	DOMINO, WILLIAM J.
<u>09383122</u>	<u>6529712</u>	150	08/25/1999	SYSTEM AND METHOD FOR AMPLIFYING A CELLULAR RADIO SIGNAL	DOMINO, WILLIAM J.
<u>09386865</u>	<u>6658237</u>	150	08/31/1999	MULTI-BAND TRANSCIVER UTILIZING DIRECT CONVERSION RECEIVER	DOMINO, WILLIAM J.
<u>09386957</u>	<u>6249190</u>	150	08/25/1999	DIFFERENTIAL DUAL COLPITTS OSCILLATOR	DOMINO, WILLIAM J.
<u>09160690</u>	<u>6614837</u>	150	09/25/1998	DEVICE SYSTEM AND	DOMINO,

				METHOD FOR LOW NOISE RADIO FREQUENCY TRANSMISSION	WILLIAM JOHN
<u>09203645</u>	<u>6415001</u>	150	12/01/1998	SYSTEM AND PROCESS FOR SHARED FREQUENCY SOURCE MULTI-BAND TRANSMITTERS AND RECEIVERS	DOMINO, WILLIAM JOHN

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name
<input type="text" value="DOMINO"/>	<input type="text" value="WILLIAM"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Friday
Date: 3/18/2005

Time: 11:07:17



PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = MAGOON

First Name = RAHUL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>09370099</u>	<u>6707326</u>	150	08/06/1999	PROGRAMMABLE FREQUENCY DIVIDER	MAGOON, RAHUL
<u>09386956</u>	<u>6587678</u>	150	08/27/1999	DIRECT CONVERSION RECEIVER EMPLOYING SUBHARMONIC FREQUENCY TRANSLATOR ARCHITECTURE AND RELATED PREPROCESSOR	MAGOON, RAHUL
<u>09398911</u>	<u>6526265</u>	150	09/14/1999	WIRELESS TRANSMITTER HAVING A MODIFIED TRANSLATION LOOP ARCHITECTURE	MAGOON, RAHUL
<u>09506302</u>	<u>6658066</u>	150	02/17/2000	METHOD AND APPARATUS FOR MULTIPLE PHASE SPLITTING FOR DUAL BAND IQ SUBHARMONIC MIXER	MAGOON, RAHUL
<u>09663848</u>	<u>6483380</u>	150	09/18/2000	GMC FILTER AND METHOD FOR SUPPRESSING UNWANTED SIGNALS INTRODUCED BY THE FILTER	MAGOON, RAHUL
<u>09668879</u>	<u>6370372</u>	150	09/25/2000	SUBHARMONIC MIXER CIRCUIT AND METHOD	MAGOON, RAHUL
<u>09811133</u>	<u>6785530</u>	150	03/16/2001	EVEN-ORDER NON- LINEARITY CORRECTION FEEDBACK FOR GILBERT STYLE MIXERS	MAGOON, RAHUL
<u>09821833</u>	Not Issued	041	03/30/2001	FREQUENCY DIVIDER WITH LOW HARMONICS	MAGOON, RAHUL
<u>09823285</u>	<u>6483391</u>	150	03/30/2001	SYSTEM FOR CONTROLLING THE AMPLITUDE OF AN OSCILLATOR	MAGOON, RAHUL
<u>09823299</u>	Not	041	03/30/2001	PHASE ADJUSTABLE	MAGOON, RAHUL

	Issued			POLYPHASE FILTERS	
<u>09823313</u>	<u>6535725</u>	150	03/30/2001	INTERFERENCE REDUCTION FOR DIRECT CONVERSION RECEIVERS	MAGOON, RAHUL
<u>09823314</u>	<u>6766158</u>	150	03/30/2001	HARMONIC CANCELLATION MIXER	MAGOON, RAHUL
<u>09823316</u>	Not Issued	030	03/30/2001	SYSTEM FOR CONTROLLING THE FREQUENCY OF AN OSCILLATOR	MAGOON, RAHUL
<u>09823679</u>	<u>6734713</u>	150	03/30/2001	SYSTEM FOR IMPROVING THE PARASITIC RESISTANCE AND CAPACITANCE EFFECT OF A TRANSISTOR-BASED SWITCH	MAGOON, RAHUL
<u>09823680</u>	<u>6671500</u>	150	03/30/2001	FREQUENCY PLAN	MAGOON, RAHUL
<u>09823681</u>	Not Issued	094	03/30/2001	LOW VOLTAGE DIGITAL INTERFACE	MAGOON, RAHUL
<u>10170507</u>	<u>6753727</u>	150	06/13/2002	SEQUENTIAL DC OFFSET CORRECTION FOR AMPLIFIER CHAIN	MAGOON, RAHUL
<u>10172176</u>	Not Issued	041	06/14/2002	GAIN COMPENSATION	MAGOON, RAHUL
<u>10210698</u>	<u>6744328</u>	150	07/31/2002	SYSTEM FOR CONTROLLING THE AMPLITUDE OF AN OSCILLATOR	MAGOON, RAHUL
<u>10233231</u>	Not Issued	094	08/30/2002	WIRELESS TRANSMITTER INCORPORATING A SYNCHRONOUS OSCILLATOR IN A TRANSLATION LOOP	MAGOON, RAHUL
<u>10261152</u>	<u>6810242</u>	150	09/30/2002	SUBHARMONIC MIXER	MAGOON, RAHUL
<u>10357125</u>	Not Issued	061	02/03/2003	SOFTWARE DEFINED MULTIPLE TRANSMIT ARCHITECTURE	MAGOON, RAHUL
<u>10359535</u>	Not Issued	030	02/06/2003	DIRECT CONVERSION RECEIVER EMPLOYING SUBHARMONIC FREQUENCY TRANSLATOR ARCHITECTURE AND RELATED PREPROCESSOR	MAGOON, RAHUL
<u>10370585</u>	Not Issued	041	02/24/2003	WIRELESS TRANSMITTER HAVING A MODIFIED TRANSLATION LOOP	MAGOON, RAHUL

				ARCHITECTURE	
<u>10712596</u>	Not Issued	030	11/13/2003	ON-CHIP VCO CALIBRATION	MAGOON, RAHUL
<u>10724071</u>	Not Issued	168	12/01/2003	METHOD AND APPARATUS FOR MULTIPLE PHASE SPLITTING FOR DUAL BAND IQ SUBHARMONIC MIXER	MAGOON, RAHUL
<u>10724116</u>	Not Issued	041	12/01/2003	METHOD AND APPARATUS FOR MULTIPLE PHASE SPLITTING FOR DUAL BAND IQ SUBHARMONIC MIXER	MAGOON, RAHUL
<u>10779879</u>	Not Issued	092	02/18/2004	PROGRAMMABLE FREQUENCY DIVIDER	MAGOON, RAHUL
<u>60539702</u>	Not Issued	159	01/28/2004	HIGH-DYNAMIC-RANGE TIME-VARYING INTEGRATED RECEIVER FOR ELIMINATION OF OFF-CHIP FILTERS	MAGOON, RAHUL
<u>60558179</u>	Not Issued	020	03/31/2004	DAT POWER CONTROL TECHNIQUES	MAGOON, RAHUL

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
<input type="text" value="MAGOON"/>	<input type="text" value="RAHUL"/>	<input type="button" value="Search"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Friday
Date: 3/18/2005

Time: 11:07:29



PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = MOLNAR

First Name = ALYOSHA

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>10724071</u>	Not Issued	168	12/01/2003	METHOD AND APPARATUS FOR MULTIPLE PHASE SPLITTING FOR DUAL BAND IQ SUBHARMONIC MIXER	MOLNAR, ALYOSHA
<u>10724116</u>	Not Issued	041	12/01/2003	METHOD AND APPARATUS FOR MULTIPLE PHASE SPLITTING FOR DUAL BAND IQ SUBHARMONIC MIXER	MOLNAR, ALYOSHA
<u>60539702</u>	Not Issued	159	01/28/2004	HIGH-DYNAMIC-RANGE TIME-VARYING INTEGRATED RECEIVER FOR ELIMINATION OF OFF-CHIP FILTERS	MOLNAR, ALYOSHA
<u>09386956</u>	<u>6587678</u>	150	08/27/1999	DIRECT CONVERSION RECEIVER EMPLOYING SUBHARMONIC FREQUENCY TRANSLATOR ARCHITECTURE AND RELATED PREPROCESSOR	MOLNAR, ALYOSHA C.
<u>09663848</u>	<u>6483380</u>	150	09/18/2000	GMC FILTER AND METHOD FOR SUPPRESSING UNWANTED SIGNALS INTRODUCED BY THE FILTER	MOLNAR, ALYOSHA C.
<u>09666501</u>	<u>6388543</u>	150	09/18/2000	SYSTEM FOR EIGHT-PHASE 45 POLYPHASE FILTER WITH AMPLITUDE MATCHING	MOLNAR, ALYOSHA C.
<u>09668879</u>	<u>6370372</u>	150	09/25/2000	SUBHARMONIC MIXER CIRCUIT AND METHOD	MOLNAR, ALYOSHA C.
<u>09811133</u>	<u>6785530</u>	150	03/16/2001	EVEN-ORDER NON-LINEARITY CORRECTION FEEDBACK FOR GILBERT STYLE MIXERS	MOLNAR, ALYOSHA C.
<u>09821833</u>	Not	041	03/30/2001	FREQUENCY DIVIDER WITH	MOLNAR,

	Issued			LOW HARMONICS	ALYOSHA C.
<u>09823285</u>	<u>6483391</u>	150	03/30/2001	SYSTEM FOR CONTROLLING THE AMPLITUDE OF AN OSCILLATOR	MOLNAR, ALYOSHA C.
<u>09823299</u>	Not Issued	041	03/30/2001	PHASE ADJUSTABLE POLYPHASE FILTERS	MOLNAR, ALYOSHA C.
<u>09823313</u>	<u>6535725</u>	150	03/30/2001	INTERFERENCE REDUCTION FOR DIRECT CONVERSION RECEIVERS	MOLNAR, ALYOSHA C.
<u>09823314</u>	<u>6766158</u>	150	03/30/2001	HARMONIC CANCELLATION MIXER	MOLNAR, ALYOSHA C.
<u>09823316</u>	Not Issued	030	03/30/2001	SYSTEM FOR CONTROLLING THE FREQUENCY OF AN OSCILLATOR	MOLNAR, ALYOSHA C.
<u>09823679</u>	<u>6734713</u>	150	03/30/2001	SYSTEM FOR IMPROVING THE PARASITIC RESISTANCE AND CAPACITANCE EFFECT OF A TRANSISTOR-BASED SWITCH	MOLNAR, ALYOSHA C.
<u>09823680</u>	<u>6671500</u>	150	03/30/2001	FREQUENCY PLAN	MOLNAR, ALYOSHA C.
<u>09823681</u>	Not Issued	094	03/30/2001	LOW VOLTAGE DIGITAL INTERFACE	MOLNAR, ALYOSHA C.
<u>10114182</u>	<u>6707342</u>	150	04/02/2002	MULTIPLE-VCO TUNING	MOLNAR, ALYOSHA C.
<u>10170507</u>	<u>6753727</u>	150	06/13/2002	SEQUENTIAL DC OFFSET CORRECTION FOR AMPLIFIER CHAIN	MOLNAR, ALYOSHA C.
<u>10172176</u>	Not Issued	041	06/14/2002	GAIN COMPENSATION	MOLNAR, ALYOSHA C.
<u>10210698</u>	<u>6744328</u>	150	07/31/2002	SYSTEM FOR CONTROLLING THE AMPLITUDE OF AN OSCILLATOR	MOLNAR, ALYOSHA C.
<u>10261152</u>	<u>6810242</u>	150	09/30/2002	SUBHARMONIC MIXER	MOLNAR, ALYOSHA C.
<u>10359535</u>	Not Issued	030	02/06/2003	DIRECT CONVERSION RECEIVER EMPLOYING SUBHARMONIC FREQUENCY TRANSLATOR ARCHITECTURE AND RELATED PREPROCESSOR	MOLNAR, ALYOSHA C.
<u>10712596</u>	Not Issued	030	11/13/2003	ON-CHIP VCO CALIBRATION	MOLNAR, ALYOSHA C.
<u>10779879</u>	Not Issued	092	02/18/2004	PROGRAMMABLE FREQUENCY DIVIDER	MOLNAR, ALYOSHA C.

<u>09261056</u>	<u>6393266</u>	150	03/02/1999	PREPROCESSOR AND RELATED FREQUENCY TRANSLATOR	MOLNAR, ALYOSHA C.
<u>09322401</u>	<u>6262623</u>	150	05/28/1999	LOG-DOMAIN FILTER HAVING A VARIABLE DYNAMIC RANGE WINDOW	MOLNAR, ALYOSHA C.
<u>09370099</u>	<u>6707326</u>	150	08/06/1999	PROGRAMMABLE FREQUENCY DIVIDER	MOLNAR, ALYOSHA C.

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name
<input type="text" value="MOLNAR"/>	<input type="text" value="ALYOSHA"/>

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)

Day : Friday
Date: 3/18/2005

Time: 11:07:42


PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = ZACHAN

First Name = JEFF

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09823285	6483391	150	03/30/2001	SYSTEM FOR CONTROLLING THE AMPLITUDE OF AN OSCILLATOR	ZACHAN, JEFF
09823316	Not Issued	030	03/30/2001	SYSTEM FOR CONTROLLING THE FREQUENCY OF AN OSCILLATOR	ZACHAN, JEFF
09823679	6734713	150	03/30/2001	SYSTEM FOR IMPROVING THE PARASITIC RESISTANCE AND CAPACITANCE EFFECT OF A TRANSISTOR-BASED SWITCH	ZACHAN, JEFF
10210698	6744328	150	07/31/2002	SYSTEM FOR CONTROLLING THE AMPLITUDE OF AN OSCILLATOR	ZACHAN, JEFF
10114182	6707342	150	04/02/2002	MULTIPLE-VCO TUNING	ZACHAN, JEFFREY M.
10806619	Not Issued	020	03/23/2004	SYSTEM FOR GENERATING AMPLITUDE MATCHED 45 DEGREE PHASE SEPARATED LOCAL OSCILLATOR SIGNALS	ZACHAN, JEFFREY M.
10849266	Not Issued	030	05/19/2004	PHASE-LOCKED LOOPS	ZACHAN, JEFFREY M.

Inventor Search Completed: No Records to Display.

Search Another: Inventor
Last Name:
First Name:

To go back use Back button on your browser toolbar.

Back to [PALM](#) | [ASSIGNMENT](#) | [OASIS](#) | [Home page](#)